

**Notice of References Cited**Application/Control No.  
09/884,312Applicant(s)/Patent Under  
Reexamination  
KELLER, RICHARD B.Examiner  
John P TrimmingsArt Unit  
2133

Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,103,451	04-1992	Fossey, Craig R.	714/757
	B	US-5,859,859	01-1999	Kim, Jong-Seon	714/801
	C	US-5,878,057	03-1999	Maa, Chia-Yiu	714/757
	D	US-4,937,828	06-1990	Shih et al.	714/757
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Guido Albertengo and Ricardo Sisto, "Parallel CRC Generation", IEEE Micro, 0272-1732/90/1000-0063501.00, October 1990
	V	"High-Speed Parallel Cyclic Redundancy Check Generator", IBM Technical Disclosure Bulletin NN901051, Vol 33, Issue 5, pp 51-56, October 1, 1990
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

